Test Engineering Internship

Full-Time • Bellport, NY

Summer 2027

Last Modified: Nov 18, 2026

Company Overview

Chiplytics provides a quality assurance platform that powers the distribution of secure, authentic electronic components. The company is the first to commercialize a scalable electrical testing method for authenticating microelectronics, combined with AI, machine vision, and supplemental inspection techniques to achieve unprecedented coverage and traceability. Chiplytics is trusted by leading defense and high-reliability manufacturers to protect the integrity of the semiconductor supply chain. For more information, visit chiplytics.com.

Job Description

We're hiring a **Test Engineer Intern** for Summer 2027 to assist in testing and equipment development for the Chiplytics inspection laboratory in Bellport, NY. Reporting to the VP of Operations, you'll help translate requirements into executable test plans, run hands-on evaluation across AS6171 methods, and contribute to our component testing lab infrastructure and workflows. You'll work closely with Product and Engineering to define requirements and deploy Chiplytics automation and inspection technology in the field.

Responsibilities

1. Complete the semiconductor inspection training program and follow approved test procedures, ESD controls, and safe component handling practices.
2. Support the management and integration of the electronic inspection workflow, including the Chiplytics Power Spectrum Analysis (PSA) system, X-ray, and XRF.
3. Manage supply chain orders, collaborate with on-site technicians on technical improvements, and help define business needs.
4. Support systems engineering for deploying R&D devices in the inspection laboratory.
5. Assist with product development for future automation and robotics technology, including machine vision and equipment management systems.
6. Prepare workstations, organize components, and maintain an orderly lab during testing assignments.
7. Document unexpected inspection results and equipment bugs to help lead operational improvement initiatives.
8. Collaborate with the Operations and R&D teams to resolve hardware and software issues, and report them to the VP of Operations and the C-suite.

Requirements

- Currently pursuing a bachelor's or master's degree in Electrical, Mechanical, Computer, Manufacturing, or a related engineering field.
- Interest in semiconductor testing, process engineering, and hands-on lab work.
- Completed basic engineering coursework, specifically statics and multivariable calculus.
- Project and/or lab experience with Design of Experiments (DOE), root-cause analysis for electromechanical designs, and Python programming.
- Strong attention to detail and the ability to follow written procedures.
- Willingness to complete required training, ask for guidance when needed, and communicate with managers.
- Availability to work at least 10 weeks (40 hours/week) on-site at our inspection lab in Bellport, NY.
- Must be a U.S. person (U.S. citizen or lawful permanent resident) due to export control regulations.

How to Apply

Send an email to join@chiplytics.com with your resume and short paragraph about yourself and why you're interested in applying.